PATENT ABSTRACTS OF JAPAN

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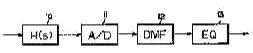
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(54) CORRELATIVE PEAK DETECTING CIRCUIT

(57) Abstract:

PROBLEM TO BE SOLVED: To provide the correlative peak detecting circuit which uses a digital matched filter(DMF) precisely detecting a correlative peak without increasing the circuit scale.



SOLUTION: A PN(pseudo-random noise) signal with its high frequency component removed by band limitation or demodulation characteristics is converted by an A/D converter 11 into a discretely temporal signal, which is inputted to the DMF 12 and becomes a discrete signal having autocorrelation characteristics in its envelope line. This signal is inputted to the correlative peak detecting circuit 13 where a specific function for approximating correlation characteristics is prepared. Here, the parameters of the specific function are solved with input and output values of the DMF to determine a function and a correlative peak is detected by finding extreme

values of the determined approximate curve.